ATP602

N-Channel Power MOSFET 600V, 5A, 2.7Ω, ATPAK

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Features

- ON-resistance $RDS(on)=2.1\Omega$ (typ.)
- 10V drive

- Input capacitance Ciss=350pF (typ.)
- Halogen free compliance

Specifications

Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	VDSS		600	V
Gate-to-Source Voltage	VGSS		±30	V
Drain Current (DC)	ID		5	А
Drain Current (Pulse)	IDP	PW≤10µs, duty cycle≤1%	15	А
Allowable Power Dissipation	PD	Tc=25°C	70	W
Channel Temperature	Tch		150	°C
Storage Temperature	Tstg		-55 to +150	°C
Avalanche Energy (Single Pulse) *1	EAS		74	mJ
Avalanche Current *2	IAV		5	А

Note :*1 VDD=99V, L=5mH, IAV=5A (Fig.1)

*2 L≤5mH, Single pulse

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

Package Dimensions

unit : mm (typ) 7057-001



Product & Package Information

- Package : ATPAK
- JEITA, JEDEC
- Minimum Packing Quantity : 3,000 pcs./reel

: -

Packing Type: TL

Marking





Electrical Connection



Electrical Characteristics at Ta=25°C

Deremotor	Symbol	Conditions	Ratings			Linit	
Parameter	Symbol	Conditions	min	typ	max	Unit	
Drain-to-Source Breakdown Voltage	V(BR)DSS	ID=10mA, VGS=0V	600			V	
Zero-Gate Voltage Drain Current	IDSS	V _{DS} =480V, V _{GS} =0V			100	μΑ	
Gate-to-Source Leakage Current	IGSS	$V_{GS}=\pm 30V$, $V_{DS}=0V$			±100	nA	
Cutoff Voltage	V _{GS} (off)	V _{DS} =10V, I _D =1mA	3		5	V	
Forward Transfer Admittance	yfs	V _{DS} =10V, I _D =2.5A	1.5	2.9		S	
Static Drain-to-Source On-State Resistance	R _{DS} (on)	ID=2.5A, VGS=10V		2.1	2.7	Ω	
Input Capacitance	Ciss			350		рF	
Output Capacitance	Coss	V _{DS} =30V, f=1MHz		68		рF	
Reverse Transfer Capacitance	Crss]		15		pF	
Turn-ON Delay Time	t _d (on)			14.2		ns	
Rise Time	tr			37.4		ns	
Turn-OFF Delay Time	t _d (off)	- See Fig.2		36.2		ns	
Fall Time	tf			20.4		ns	
Total Gate Charge	Qg			13.6		nC	
Gate-to-Source Charge	Qgs	V _{DS} =200V, V _{GS} =10V, I _D =5A		3.4		nC	
Gate-to-Drain "Miller" Charge	Qgd			7.2		nC	
Diode Forward Voltage	V _{SD}	IS=5A, VGS=0V		0.9	1.2	V	

Fig.1 Avalanche Resistance Test Circuit



Fig.2 Switching Time Test Circuit



Ordering Information

Device	Package	Shipping	memo
ATP602-TL-H	ATPAK	3,000pcs./reel	Pb Free and Halogen Free





Taping Specification ATP602-TL-H

1. Packing Format (TL)

Dackage Name	Carrier Tape	Maximum Number of devices contained (pcs)			Packing format		
Larrage Mame	Туре	Reel	Inner box	Outer box	INNER BOX SD-C-18	OUTER BOX SD-A-18	
					1 reels contained	5 inner boxes contained	
ATPAK	ATP	3,000	3,000	15,000	Dimensions:mm (external)	Dimensions:mm (external)	
					340×340×28	355×355×165	



Packing method

Reel	label,	Inner	box	label
(unit:mm)				
	1	69		1

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Outer box label It is a label at the time of factory shipments. The form of a label may change in physical distribution process.

		L		108
				-
			TYPE CODE	*000000000*
			TYPE	00000000
0			QTY	0, 000 pcs (1) LEAD FREE #
8 (lot	00000000
		PACKAGE	00000000	
-		SI	PECIAL	#11##1##1#############################
		1	ASSEMBLY	ex+* (DIFFUSION:*****)

NOTE (1) The LEAD FREE 🕷 description shows that the surface treatment of the terminal is lead free.

Label	JEITA Phase
LEAD FREE 3	JEITA Phase 3A
LEAD FREE 4	JEITA Phase 3

2. Taping configuration

2-1. Carrier tape size (unit:mm)



The one erectrode terminals on feed hole side TL

Outline Drawing ATP602-TL-H



Land Pattern Example



Note on usage : Since the ATP602 is a MOSFET product, please avoid using this device in the vicinity of highly charged objects.

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